

US007658772B2

(12) United States Patent

Tavkhelidze et al.

(54) PROCESS FOR MAKING ELECTRODE PAIRS

(75) Inventors: Avto Tavkhelidze, Tbilisi (GE); Stuart

Harbron, Berkhamsted (GB)

(73) Assignee: Borealis Technical Limited (GI)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 425 days.

This patent is subject to a terminal dis-

claimer.

(21) Appl. No.: 11/254,495

(22) Filed: Oct. 20, 2005

(65) **Prior Publication Data**

US 2006/0038290 A1 Feb. 23, 2006

Related U.S. Application Data

- (63) Continuation-in-part of application No. 10/234,498, filed on Sep. 3, 2002, now Pat. No. 7,140,102, application No. 11/254,495, filed on Oct. 20, 2005, which is a continuation-in-part of application No. 10/507,273, filed as application No. PCT/US03/07015 on Mar. 6, 2003, now Pat. No. 7,169,006, which is a continuation-in-part of application No. 10/823,483, filed on Apr. 12, 2004, now abandoned, which is a continuation-in-part of application No. 09/481,803, filed on Aug. 31, 1998, now Pat. No. 6,720,704, which is a continuation-in-part of application No. 08/924,910, filed on Sep. 8, 1997, now abandoned.
- (60) Provisional application No. 60/316,918, filed on Sep. 2, 2001, provisional application No. 60/362,494, filed on Mar. 6, 2002, provisional application No. 60/373, 508, filed on Apr. 17, 2002.

(30) Foreign Application Priority Data

Oct. 25, 2004 (GB) 0423534.7

(10) Patent No.: US 7,658,772 B2 (45) Date of Patent: *Feb. 9, 2010

(51) **Int. Cl.**

H01L 21/00 (2006.01) H05K 3/30 (2006.01)

(52) **U.S. Cl.** **29/25.02**; 29/831; 29/842; 29/847; 427/250; 427/455

See application file for complete search history.

(56) References Cited

U.S. PATENT DOCUMENTS

2,510,397 A 6/1950 Hansell

(Continued)

FOREIGN PATENT DOCUMENTS

DE 3404137 A1 8/1985

(Continued)

OTHER PUBLICATIONS

Chou et al., "Imprint Lithography with 25 Nanometer Resolution", Science, Apr. 5, 1996, pp. 85-87, vol. 272.

(Continued)

Primary Examiner—C. J Arbes

(57) ABSTRACT

The present invention is a process for making a matching pair of surfaces, which involves creating a network of channels on one surface of two substrate. The substrates are then coated with one or more layers of materials, the coating extending over the regions between the channels and also partially into the channels. The two coated surfaces are then contacted and pressure is applied, which causes the coatings to be pressed into the network of channels, and surface features on one of the layers of material creates matching surface features in the other, and vice versa. It also results in the formation of a composite. In a final step, the composite is separated, forming a matching pair of surfaces.

17 Claims, 2 Drawing Sheets

